# (FILE 'HOME' ENTERED AT 08:39:39 ON 11 MAR 2002)

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FILE 'USPATFULL' ENTERED AT 08:42:11 ON 11 MAR 2002
L1
          61093 S (LCD OR LIQUID CRYSTAL DISPLAY)
          14221 S L1 AND (INTEGRATED CIRCUIT OR IC)
L2
L3
            232 S L2 AND (WAVE NOISE OR ELECTROMAGNETIC INTERFERENCE)
L4
              1 S L3 AND (SERIAL TRANSFER? (4A) (DATA OR INFORMATION))
L5
             25 S L2 AND (SERIAL TRANSFER? (4A) (DATA OR INFORMATION))
L6
             0 S L5 AND ((TIME (2A) (DELAY OR LAG)) (3A) (INCREMENT? OR
INTERV
L7
             50 S L1 AND (SERIAL TRANSFER? (4A) (DATA OR INFORMATION))
L8
             0 S L7 AND ((TIME (2A) (DELAY OR LAG)) (3A) (INCREMENT? OR
INTERV
             10 S L7 AND (TIME (2A) (DELAY OR LAG))
L9
L10
             8 S L5 AND (TIME (2A) (DELAY OR LAG))
              0 S L10 AND (MULTI? DATA PORT)
L11
L12
              1 S L10 AND (DATA PORT)
L13
            275 S L1 AND ((TIME (2A) (DELAY OR LAG)) (3A) (INCREMENT? OR
INTERV
L14
             88 S L13 AND (INTEGRATED CIRCUIT OR IC)
L15
             0 S L14 AND (SERIAL TRANSFER (4A) (DATA OR INFORMATION))
L16
              0 S L14 AND (SERIAL TRANSFER)
             0 S L13 AND (SERIAL TRANSFER (4A) (DATA OR INFORMATION))
L17
L18
             10 S L13 AND (DATA PORT)
              9 S L18 AND (NOISE OR WAVE NOISE OR ELECTROMAGNETIC
L19
INTERFERENCE
L20
              0 S L7 AND (INCREMENT? (6A) (TIME (2A) (DELAY OR LAG)))
L21
             0 S L5 AND (INCREMENT? (6A) (TIME (2A) (DELAY OR LAG)))
L22
             21 S L2 AND (INCREMENT? (6A) (TIME (2A) (DELAY OR LAG)))
L23
            844 S ((TIME (W) (DELAY OR LAG)) (4A) (INCREMEN? OR STEPS OR
LITTLE
L24
             0 S L23 AND L3
            14 S L23 AND L2
L25
L26
            472 S ((TIME (W) (DELAY OR LAG)) (4A) (INCREMEN? OR LITTLE BY
LITTL
L27
             2 S L26 AND L2
L28
            14 S L1 AND L26
L29
             0 S L28 AND (SERIAL TRANSFER)
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# (FILE 'HOME' ENTERED AT 16:52:41 ON 29 SEP 2001)

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FILE 'USPATFULL' ENTERED AT 16:52:56 ON 29 SEP 2001
L1
           2310 S (RED OR GREEN OR BLUE) (6A) (BIT)
          18962 S (RED OR GREEN OR BLUE) (6A) (DISPLAY DATA OR DATA OR SIGNAL)
L2
L3
           1325 S L2 AND (TIMING (2A) CIRCUIT)
L4
             73 S L3 AND (THIN FILM TRANSISTOR OR TFT)
L5
             5 S L4 AND L1
L6
             23 S L4 AND (DELAY OR LAG)
L7
             2 S L5 AND (DELAY OR LAG)
           180 S (DETEC? (6A) POLARITY (4A) BIT)
L8
L9
             0 S L8 AND L6
L10
             0 S L8 AND L4
L11
             0 S L8 AND L3
L12
             4 S L8 AND L1
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FILE 'USPATFULL' ENTERED AT 20:06:25 ON 28 SEP 2001
           9212 S (DRIV? (6W) (LIQUID CRYSTAL DISPLAY OR LIQUID CRYSTAL OR
L1
LCD)
L2
             16 S (RED OR GREEN OR BLUE) (4A) (TIMING CIRCUIT)
L3
            234 S (RED OR GREEN OR BLUE) (S) (TIMING CIRCUIT)
L4
            237 S (RED OR GREEN OR BLUE) (P) (TIMING CIRCUIT)
L5
             0 S L4 (P) (THIN FILM TRANSISTOR OR TFT)
L6
              7 S L1 AND L4
L7
             32 S L4 AND (TIME (W) (LAG OR DELAY))
L8
              0 S L7 AND L1
L9
          45129 S (DRIV? AND (LIQUID CRYSTAL DISPLAY OR LIQUID CRYSTAL OR
LCD))
L10
             22 S L9 AND L4
             0 S L10 AND (TIME (W) (LAG OR DELAY))
L11
L12
          78524 S (TIME (W) (LAG OR DELAY))
L13
           2339 S L12 AND L9
L14
            235 S L13 AND (RED AND GREEN AND BLUE)
             0 S L13 AND ((RED OR GREEN OR BLUE) (6A) (TIM? CIRCUIT))
L15
L16
            300 S L13 AND (TIM? CIRCUIT)
L17
             28 S L16 AND (RED AND GREEN AND BLUE)
L18
              0 S L6 AND (TIME (W) (LAG OR DELAY))
           1104 S (TIMING CIRCUIT AND (LIQUID CRYSTAL DISPLAY OR LCD OR
L19
LIQUID
L20
           192 S L19 AND L12
L21
             0 S L20 AND L4
L22
             19 S L20 AND (RED AND GREEN AND BLUE)
          3216 S L1 AND (TFT OR THIN FILM TRANSISTOR)
L23
             0 S L23 AND ((RED OR GREEN OR BLUE) (6A) (TIM? CIRCUIT))
L24
             63 S ((RED OR GREEN OR BLUE) (6A) (TIM? CIRCUIT))
L25
L26
            28 S L25 AND (LIQUID CRYSTAL DISPLAY OR LCD)
          1883 S (RED OR GREEN OR BLUE) (4A) (BIT)
L27
             1 S TIMING CIRCUIT (4A) (TFT OR THIN FILM TRANSISTOR)
L28
L29
             8 S TIMING CIRCUIT (P) (TFT OR THIN FILM TRANSISTOR)
L30
            28 S L17 AND L9
L31
             0 S L27 AND L29
         54174 S (LCD OR LIQUID CRYSTAL DISPLAY)
L32
L33
           918 S L32 AND (TIMING CIRCUIT)
L34
           160 S L33 AND (TIM? DELAY OR TIM? LAG)
L35
            59 S L34 AND (RED OR GREEN OR BLUE)
            18 S L34 AND (RED AND GREEN AND BLUE)
L36
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### (FILE 'HOME' ENTERED AT 22:40:04 ON 27 SEP 2001)

#### FILE 'USPATFULL' ENTERED AT 22:41:47 ON 27 SEP 2001 SET HIGHLIGHT OFF SET HIGHLIGHT ON 54174 S LIQUID CRYSTAL DISPLAY OR LCD L1L2597 S ((INTEGRATED CIRCUIT OR IC) (1P) (MULTI (6A) OUTPUT)) L3 30 S L1 AND L2 L40 S L3 AND (DATA INPUT SIGNAL) L5 14 S L3 AND (INPUT SIGNAL) L6 128 S ((INTEGRATED CIRCUIT OR IC) (1P) (MULTI (6A) OUTPUT) (1P) (D 30 S ((INTEGRATED CIRCUIT OR IC) (1P) (MULTI (1A) OUTPUT) L7(1P) (D 0 S ((INTEGRATED CIRCUIT OR IC) (1P) (MULTI PORT OUTPUT) $^{18}$ (1P) (D 7 S MULTI PORT OUTPUT L9 L101665 S ((INTEGRATED CIRCUIT OR IC) (1P) ((MULTI? OR PLURAL?) (2A) OU L1174 S L10 AND L1

# (FILE 'HOME' ENTERED AT 16:35:46 ON 28 SEP 2001)

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FILE 'USPATFULL' ENTERED AT 16:38:42 ON 28 SEP 2001
L1
          54174 S (LIQUID CRYSTAL DISPLAY OR LCD)
1.2
         214101 S (INTEGRATED CIRCUIT OR IC)
        363146 S ((OUTPUT SIGNAL OR DATA OUTPUT OR OUTPUT OR ((MULTI? OR
L3
PLURA
L4
           882 S L1 (1P) L2 (1P) L3
            43 S L4 AND (ELECTROMAGNETIC INTERFERENCE OR EMI)
L5
L6
            17 S L5 AND (CLOCK SIGNAL OR CLOCK PULSE)
             1 S L6 AND (TIME LAG OR (TIME (4A) LAG))
L7
         84693 S ((OUTPUT SIGNAL OR DATA OUTPUT OR OUTPUT (4A) ((MULTI? OR
L8
PLU
          3553 S ((ELECTROMAGNETIC INTERFERENCE OR EMI) (1S) (REDUC?))
L9
           207 S L1 AND L9
L10
            97 S L10 AND L2
L11
L12
            81 S L11 AND L3
         63611 S ((OUTPUT SIGNAL OR DATA OUTPUT OR OUTPUT) (4A) ((MULTI? OR
L13
PL
         34272 S ((OUTPUT SIGNAL OR DATA OUTPUT OR OUTPUT) (4A) ((MULTI OR
L14
MUL
L15
          1856 S L2 (1S) L14
L16
             1 S L15 (1S) L9
L17
             0 S L15 (1S) L11
L18
            31 S L15 (1S) L1
L19
             0 S L18 AND L9
L20
            97 S L1 AND L2 AND L9
          837 S L14 (4A) (INPUT SIGNAL OR DATA INPUT)
L21
L22
           0 S L20 AND L21
L23
         10658 S L2 AND L14
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